

**Amendments to the Specification:**

Please amend paragraph [0069] as shown below:

[0069] Specifically, the CCD control unit 19 provides a transfer signal for making operate the shift registers 35 to 37 to the monochrome ~~CDD~~ CCD image sensor 30 to control the transfer operation of the shift registers 35 to 37 and thereby to perform reading of an image in the primary direction. Also, the CPU 13 relatively moves the original copy or the CCD image sensor 3 in the secondary scanning direction through a motor control unit for making operate a not shown original copy transfer mechanism or a reading unit to perform reading of the image in two-dimensional manner.

Please amend paragraph [0073] as shown below:

[0073] The analog frontend IC 5 is provided mainly with correlated double sampling circuits (CDS) 41 to 43, offset adjustment circuits 44 to 46, programmable gain amplifiers (PGA) 47 to 49, the above-mentioned multiplexer (MUX) 7, the [[the]] above-mentioned analog/digital converter (ADC) 9, an ADC operation determination unit 51, an interface 53 and a register unit 55. The analog frontend IC 5 has three channels, and the correlated double sampling circuits 41 to 43, the offset adjustment circuits 44 to 46 and the programmable gain amplifiers 47 to 49 are provided for the respective channels.